



Rev. 08/22/2003

Attorney Docket No. MTEC-008/00US
Client Reference No.: 0126.00US

PATENT

Express Mail Label Number:
Date of Deposit:

EV525577257 US
November 10, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of THEKKATH et al.

Serial No.: 09/850,195

Examiner: Baderman, Scott T.

Confirmation No.: 9739

Art Unit: 2113

Filed: May 8, 2001

For: **SYSTEM AND METHOD FOR SPEEDING UP EJTAG
BLOCK DATA TRANSFERS**

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Enclosed is an Information Disclosure Statement and accompanying Form PTO/SB/08A for the above-identified patent application.

- ☒ In accordance with 37 C.F.R. §1.97(c), also enclosed is:
- ☒ the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p); or
- ☐ a statement as specified in 37 C.F.R. §1.97(e).
- ☒ Check to cover the fee is attached.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 03-3117.

Dated: Nov. 10, 2005

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Respectfully submitted,
COOLEY GODWARD LLP

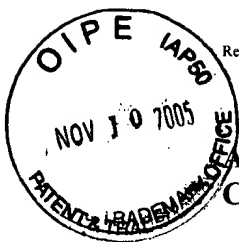
By: _____

William S. Galliani
Reg. No. 33,885

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Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(c)

In accordance with the duty of disclosure set forth in 37 C.F.R. §1.56,
Applicant(s) hereby submits the following information in conformance with 37 C.F.R.
§§1.97 and 1.98.

- ☒ Pursuant to 37 C.F.R. §1.98, a copy of each document **P4-P5, D1-18** cited in the attached Form PTO/SB/08A is enclosed.
- ☐ No copies of the publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98(d) because the publications were previously cited by or submitted to the Office in prior Application Serial No. ___ to which the above-identified application claims priority under 35 U.S.C. §120.
- ☒ No copies of any U.S. patents or U.S. patent application publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98.

- ☐ Publication(s) ___ listed on the attached Form PTO/SB/08A were cited in a foreign search or examination report corresponding to ___ application serial no. ___ and mailed on ___.
- ☐ Enclosed is a copy of a non-English publication(s) ___. Pursuant to §609 of the M.P.E.P., Applicant submits the attached foreign search or examination report, which cites such non-English language publication(s).
- ☐ Enclosed is a copy of a non-English publication(s) ___. English language publication ___ (copy enclosed) claims priority from this non-English publication.
- ☐ Enclosed is an explanation of non-English publication(s) ___ for which an English translation is not available.
- ☐ Enclosed is an English translation of non-English publication(s) ___ cited in the attached Form PTO/SB/08A.
- ☐ Enclosed is a copy of pending patent Application Serial No. ___.

This Information Disclosure Statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing of:

- ☒ a final action under 37 C.F.R. §1.113;
- ☐ a notice of allowance under 37 C.F.R. §1.311; or
- ☐ an action that otherwise closes prosecution in this application.

In accordance with 37 C.F.R. §1.97(c) also enclosed is:

- ☒ Fee under 37 C.F.R. §1.17(p) in the amount of \$180.00; or

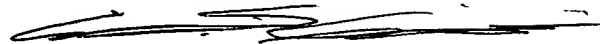
It is respectfully requested that the Examiner consider the above-noted information and return an initialed copy of the attached Form PTO/SB/08A to the undersigned.

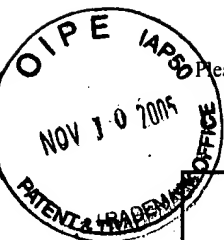
Dated: Nov. 10, 2005

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Respectfully submitted,
COOLEY GODWARD LLP

By: _____


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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/850,195
Filing Date	May 8, 2001
First Named Inventor	THEKKATH
Group Art Unit	2113
Examiner Name	Baderman, Scott T.
Attorney Docket Number	MTEC-008/00US

Sheet	1	of	3
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code ² (if known)		
	P1	5,084,814		Vaglica et al.	01/28/1992
	P2	6,032,268		Swoboda et al.	09/29/2000
	P3	6,687,865		Dervisoglu et al.	02/23/2004
	P4	6,754,804		Hudepohl et al.	06/22/2004
	P5	09/751,747		Hudepohl et al.	12/29/2000
	P6	09/751,748		Hudepohl et al.	12/29/2000
	P7	09/844,671		Thekkath et al.	04/30/2001
	P8	09/944,669		Thekkath	04/30/2001
	P9	09/894,832		Thekkath et al.	06/29/2001
	P10	09/844,668		Thekkath et al.	04/30/2001
	P11	09/844,672		Thekkath	04/30/2001
	P12	09/844,673		Thekkath	04/30/2001
	P13	09/844,271		Thekkath	04/30/2001
	P14	09/894,831		Treue et al.	06/29/2001
	P15	09/894,830		Treue et al.	06/29/2001
	P16	09/844,670		Thekkath	04/30/2001

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)			

Examiner Signature		Date Considered	
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¹ Unique citation designation number.

² See attached Kinds of U.S. Patent Documents.

³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible.

⁶ Applicant is to place a check mark here if English language Translation is attached.



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PTO/SB/08A (08-00)

Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known		
			Application Number	09/850,195	
			Filing Date	May 8, 2001	
			First Named Inventor	THEKKATH	
			Group Art Unit	2113	
Examiner Name	Baderman, Scott T.				
Sheet	1	of	2	Attorney Docket Number	MTEC-008/00US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	D1	F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.	
	D2	FALOUTSOS, CHRISTOS et al., "Description and Performance Analysis of Signature File Methods for Office Filing", <i>ACM Transactions on Office Information Systems</i> , (July 1987), 5:3:237-257.	
	D3	Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, <i>1990 ACM</i> , pp. 37-47.	
	D4	M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. I-29.	
	D5	ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.	
	D6	A. Srivastaba et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.	
	D7	ATOM User Manual, Digital Equipment Corporation, March 1994, pp. 1-28.	
	D8	B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.	
	D9	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> , Vol. 29, No. 2, June 1997, pp. 128-170.	
	D10	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 <i>IEEE Service Center</i> , 12 pp.	
	D11	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 <i>ACM 1-58113-083</i> , pp. 214, 215.	
	D12	MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide," [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.	
	D13	Darren JONES, "Opal Coprocessor Interface," <i>MIP Proprietary/Confidential</i> , June 4, 1999, pp. 1-18.	
	D14	Darren JONES, <i>MIPSS64™ 5K™ Processor Cores User's Manual</i> , Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.	

	D15	<i>MIPSS64™ 5K™ Processor Cores User's Manual</i> , Rev. 1.0.1., 1999, pp. 247-276.	
	D16	Embedded Trace Macrocell, (Rev. 1) Specification, (2000).	
	D17	MIPS Technologies; "MIPS64™ 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.	
	D18	MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.	

Examiner Signature		Date Considered	
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